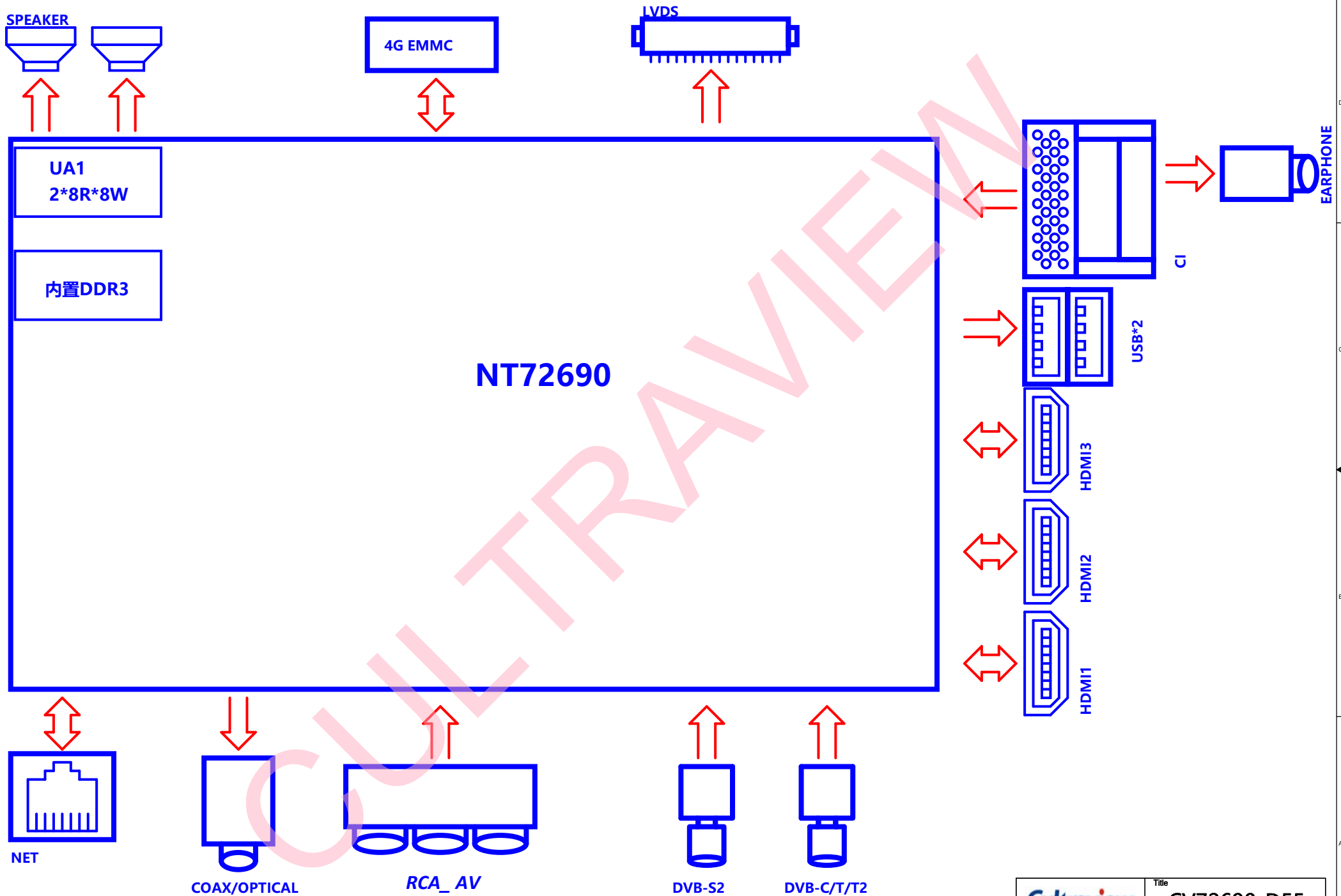
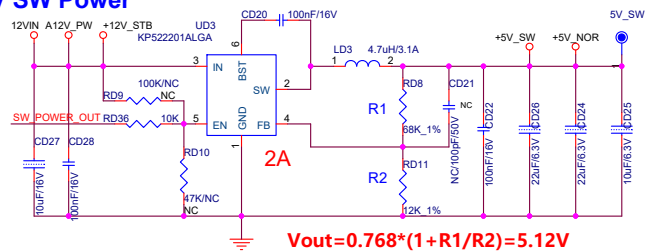
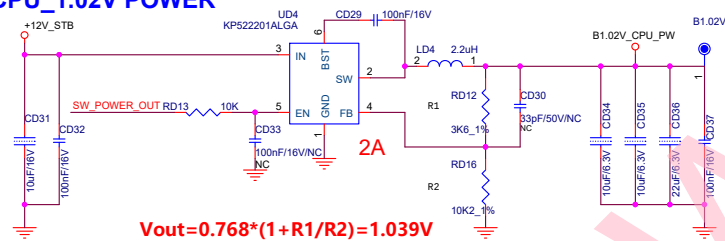




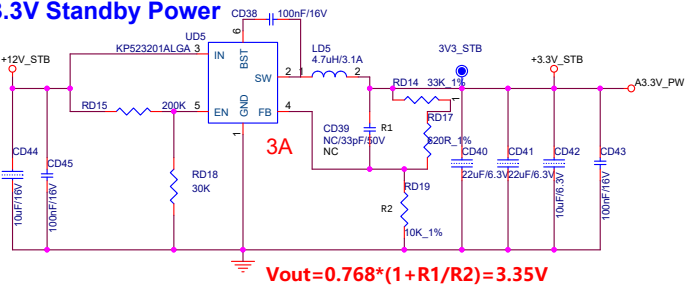
5V SW Power



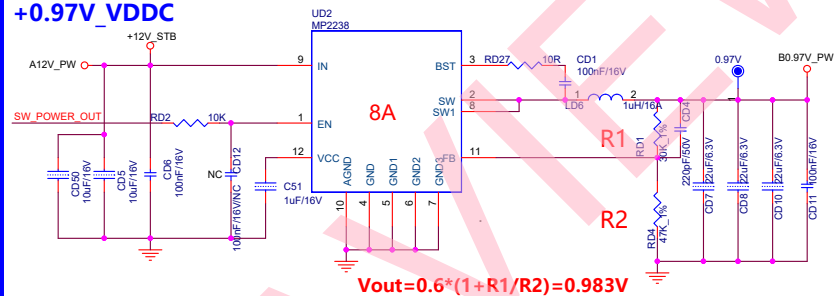
CPU_1.02V POWER



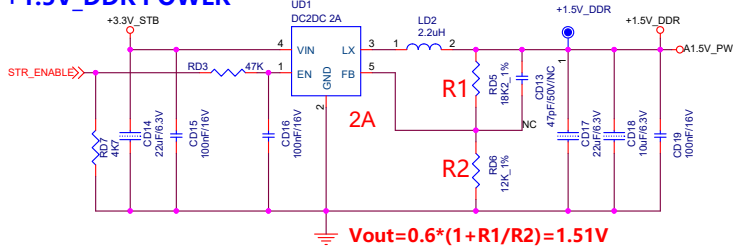
3.3V Standby Power



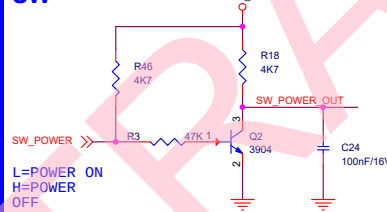
+0.97V_VDDC



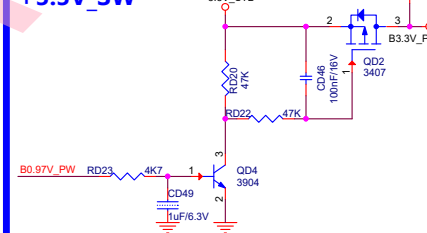
+1.5V_DDR POWER



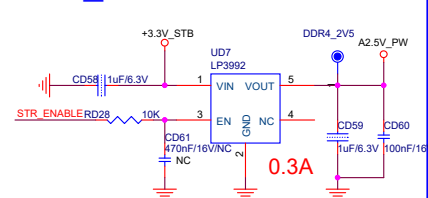
SW



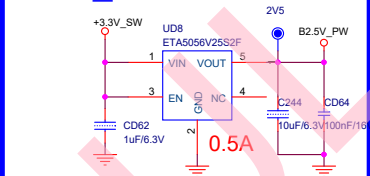
+3.3V_SW



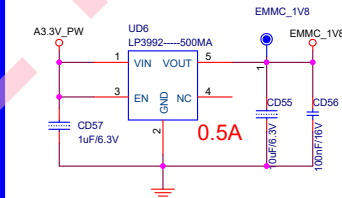
DDR4_2.5V



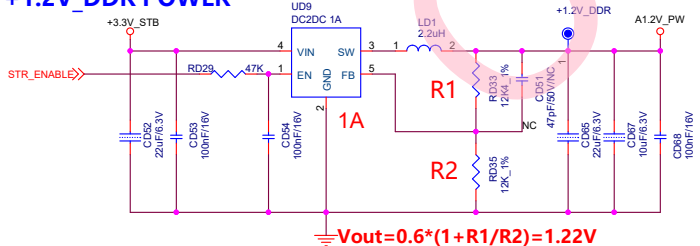
MAIN_2.5V

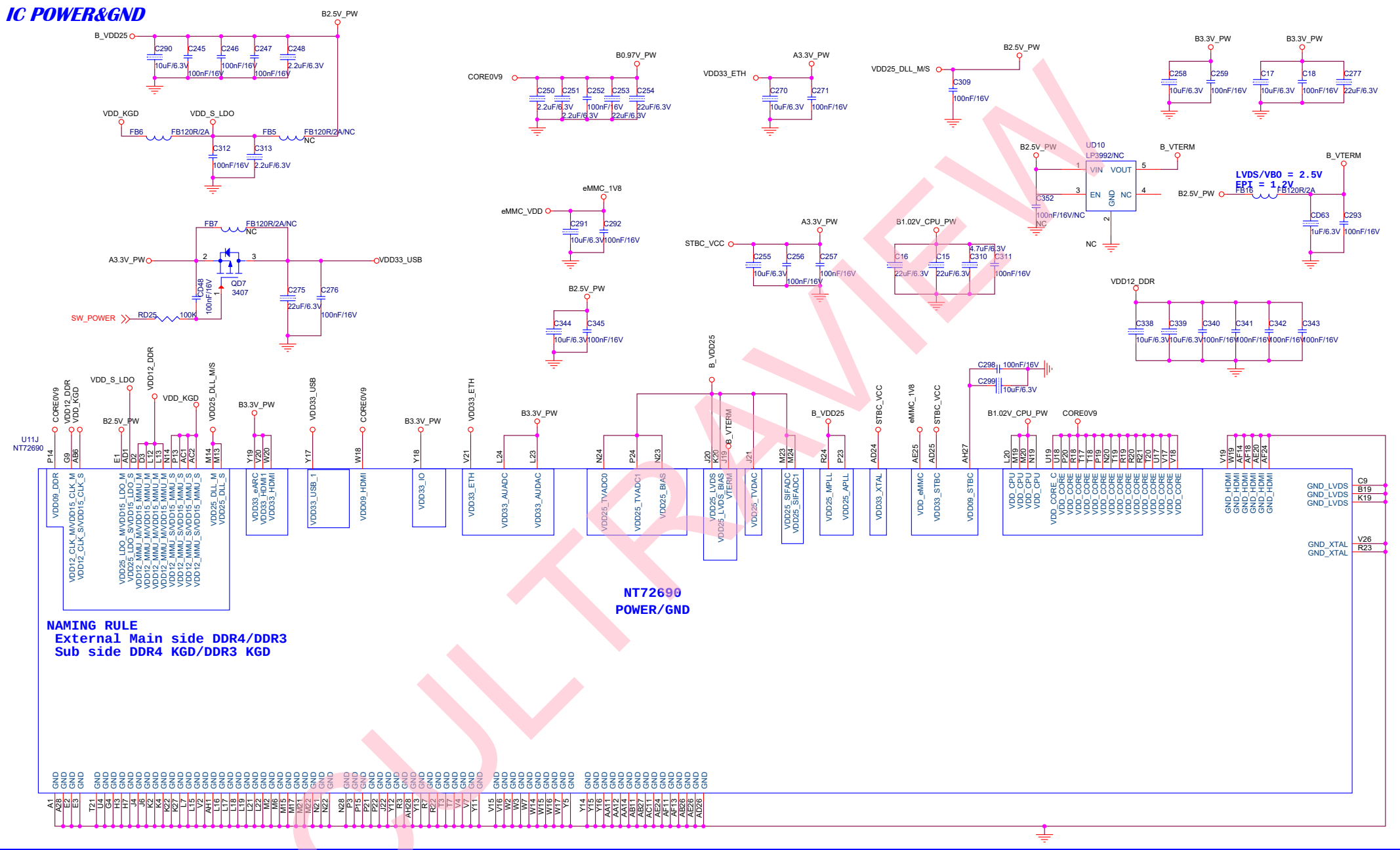


EMMC 1.8V

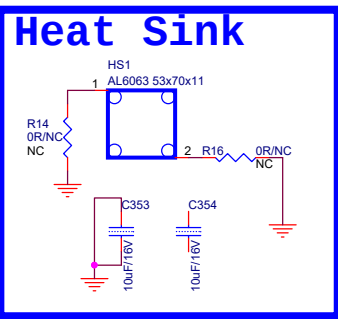
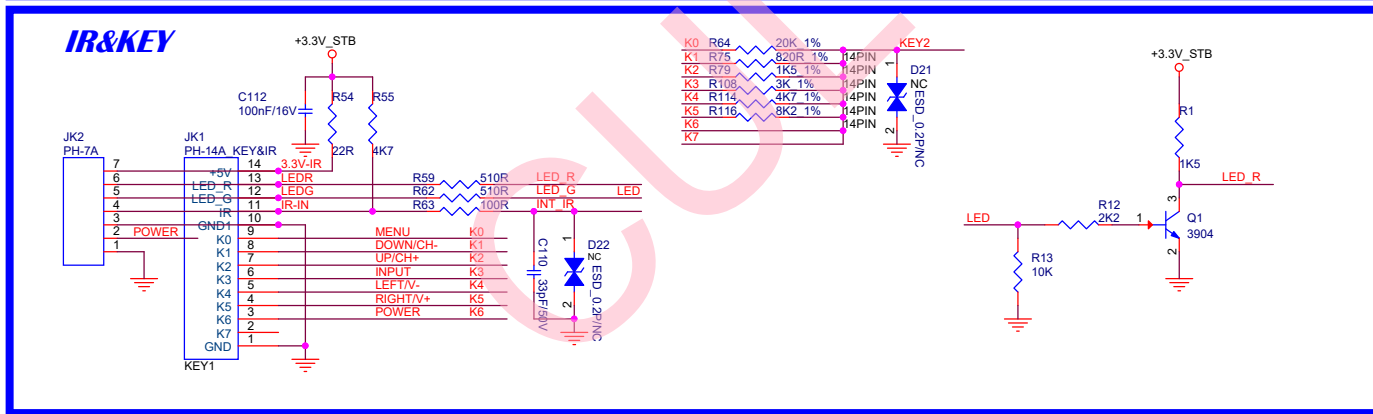
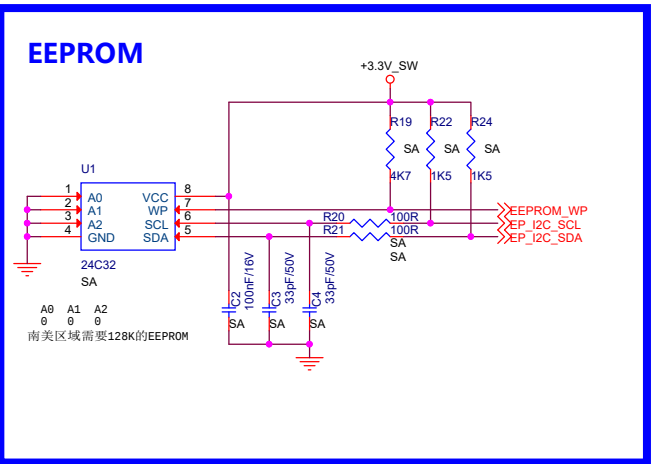
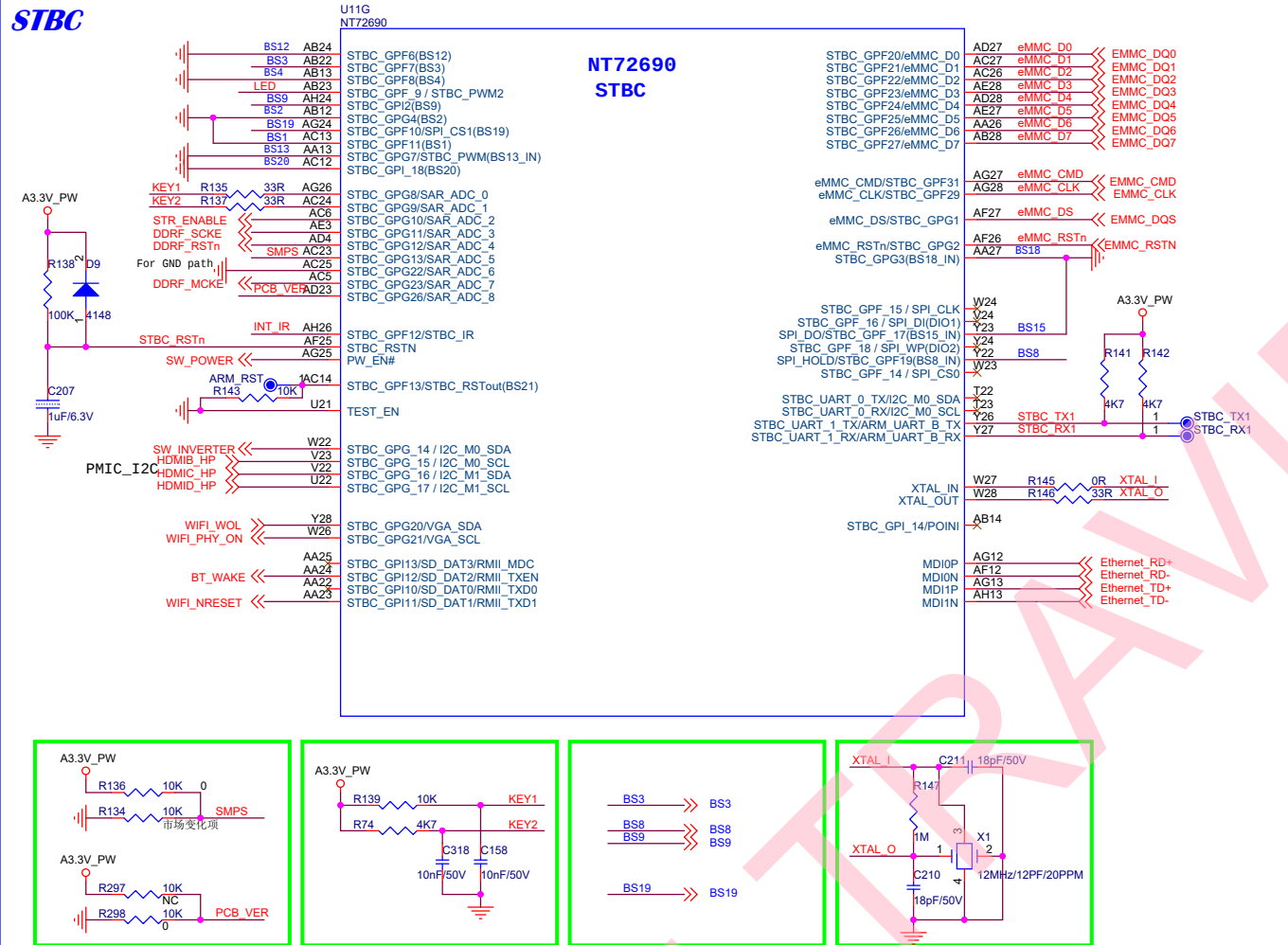


+1.2V_DDR POWER

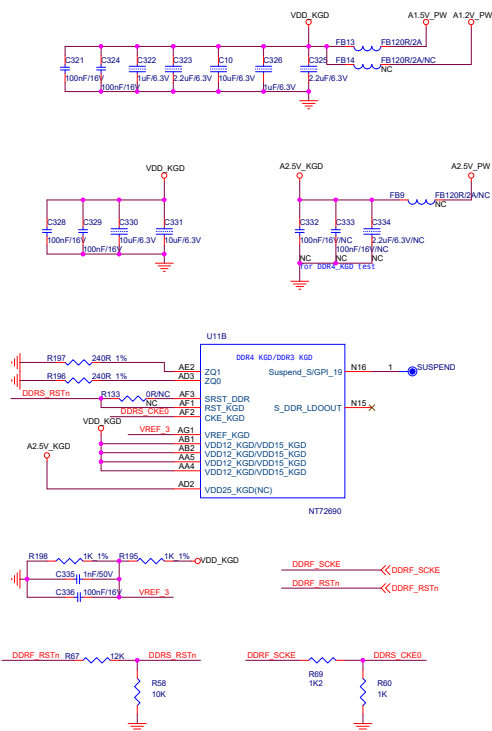


IC POWER&GND

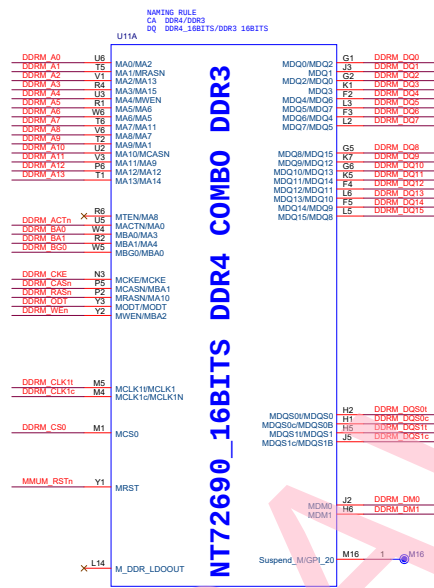
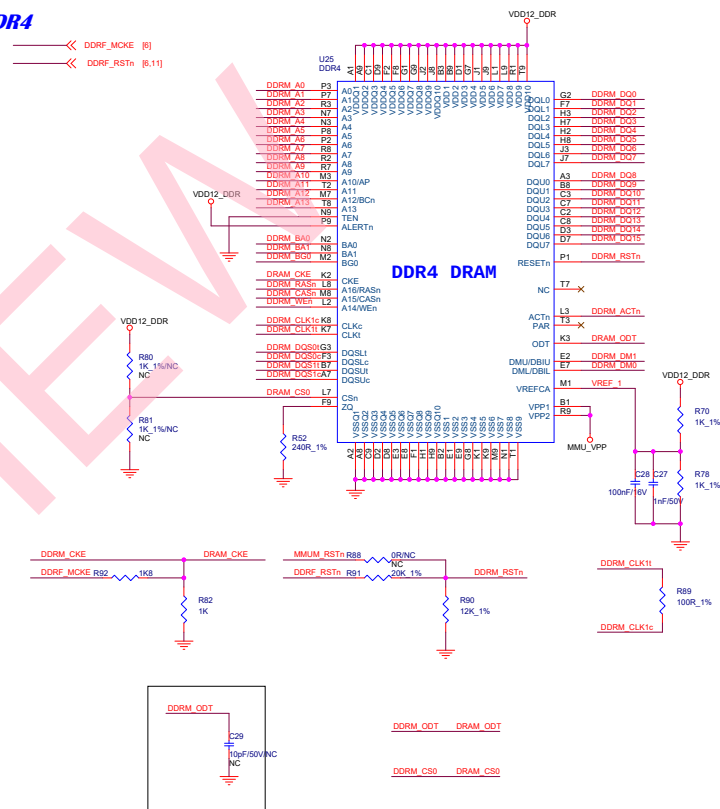
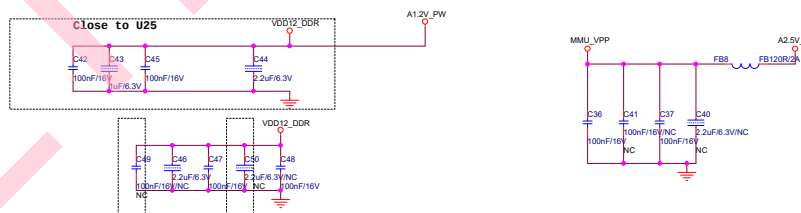
NAMING RULE
External Main side DDR4/DDR3
Sub side DDR4 KGD/DDR3 KGD

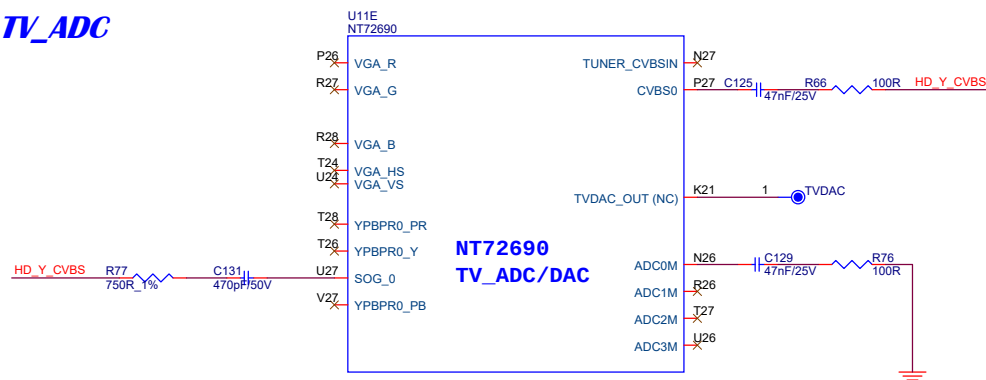


DDR_S

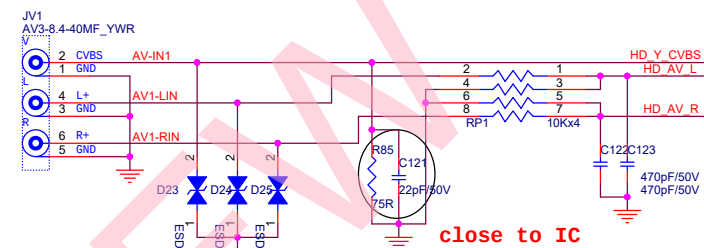
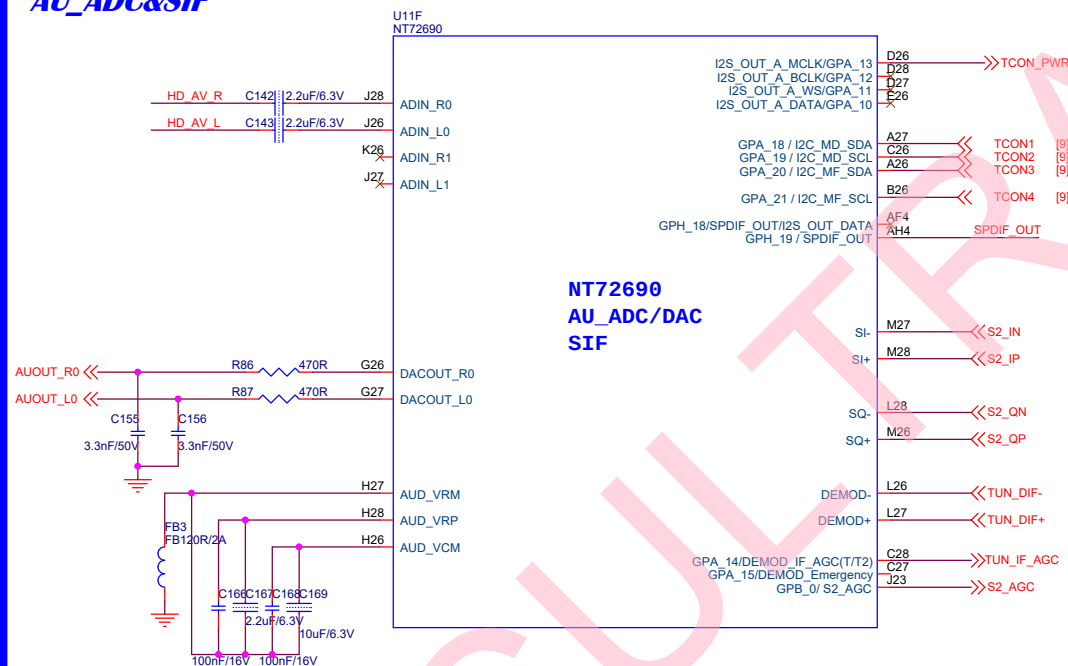


DDR_M

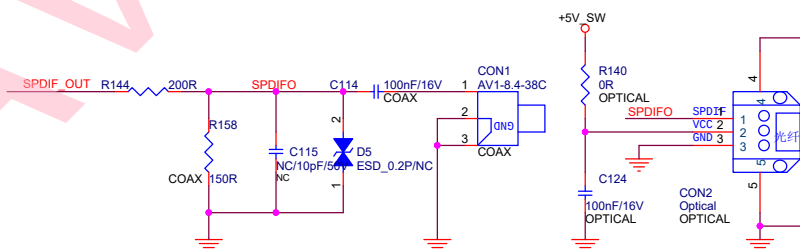
**DDR4****DDR4_POWER**

TV_ADC

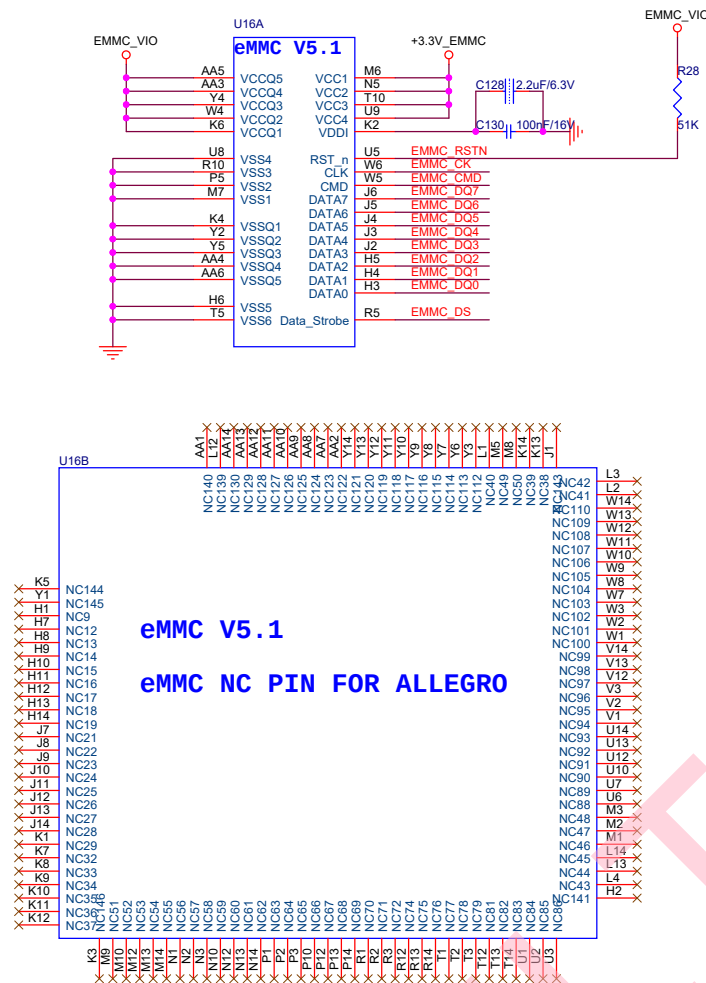
AV1_IN

***AU_ADC&SIF***

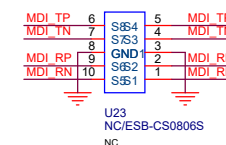
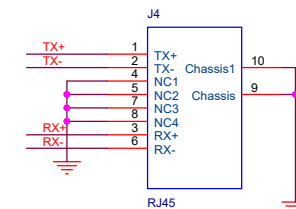
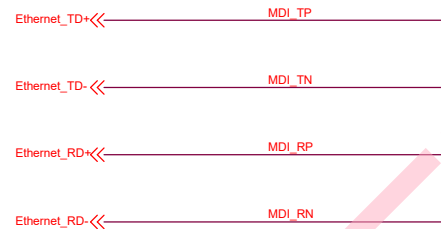
SPDIF OUT



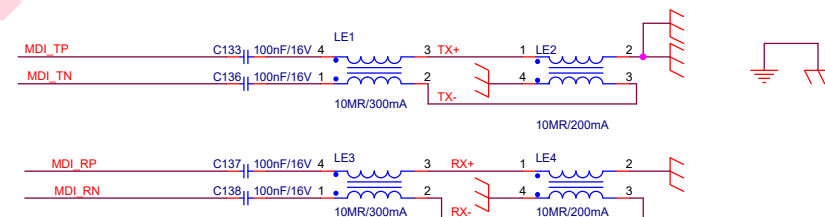
EMMC



Ethernet Connetor



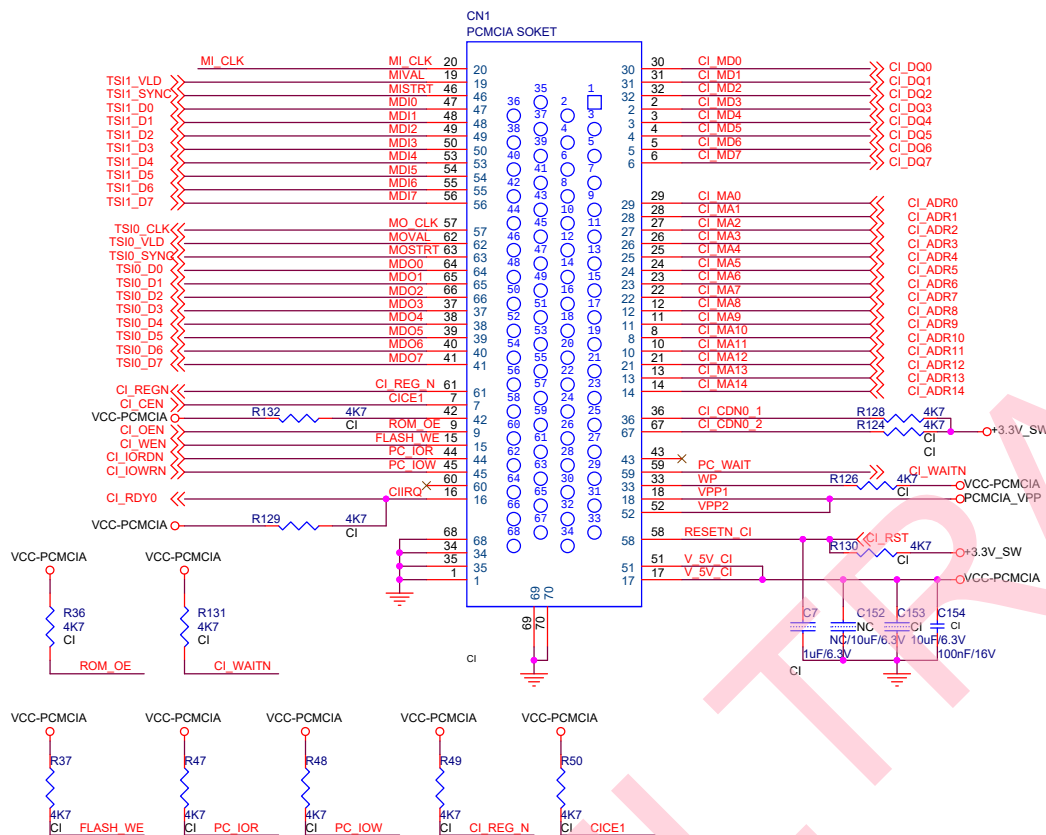
ENTHERNET 分立



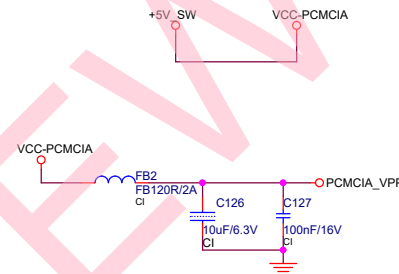
EMMC Power



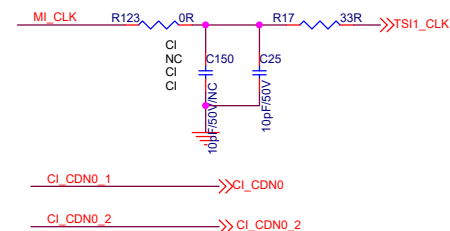
CI Socket

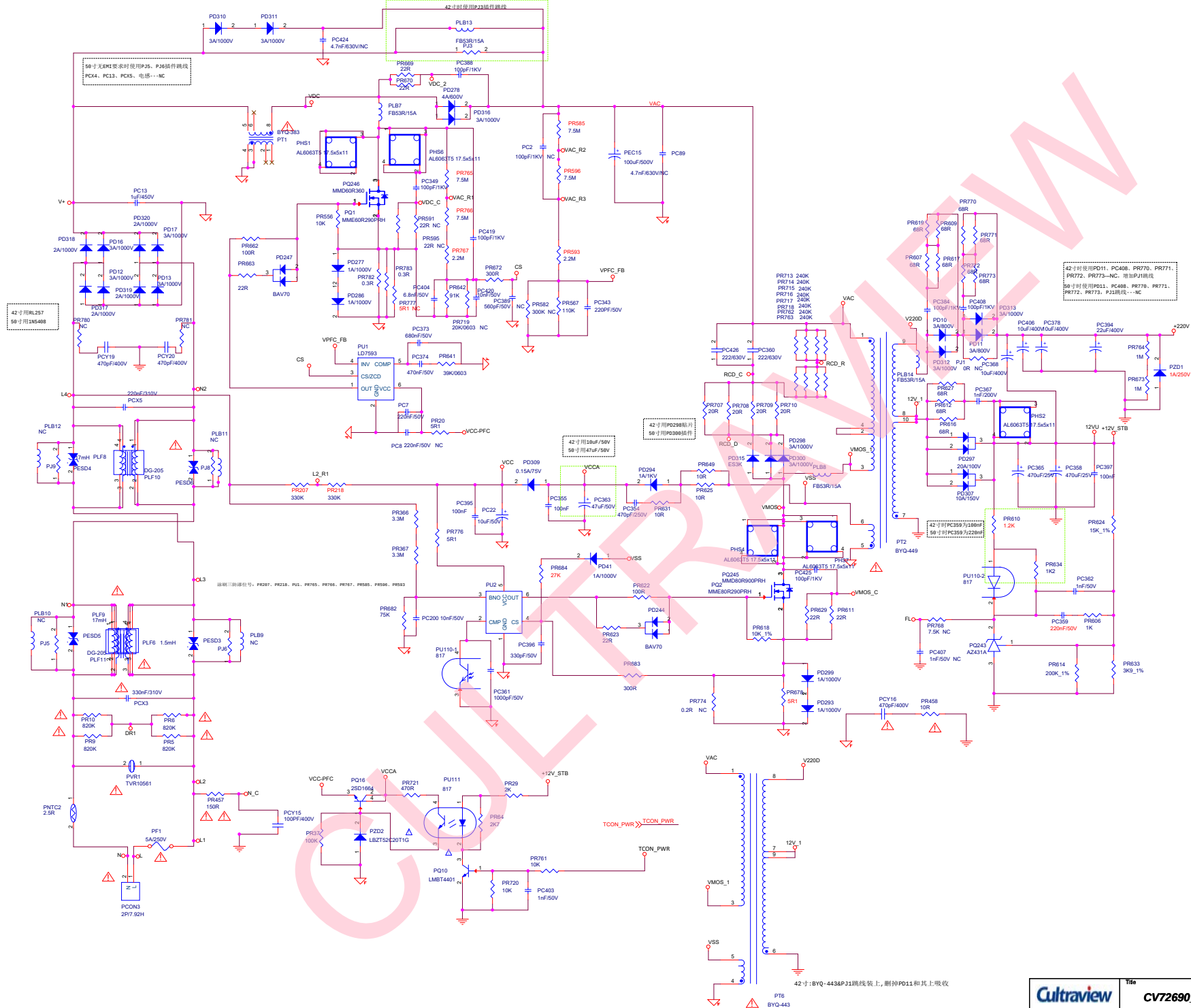


PCMCIA POWER

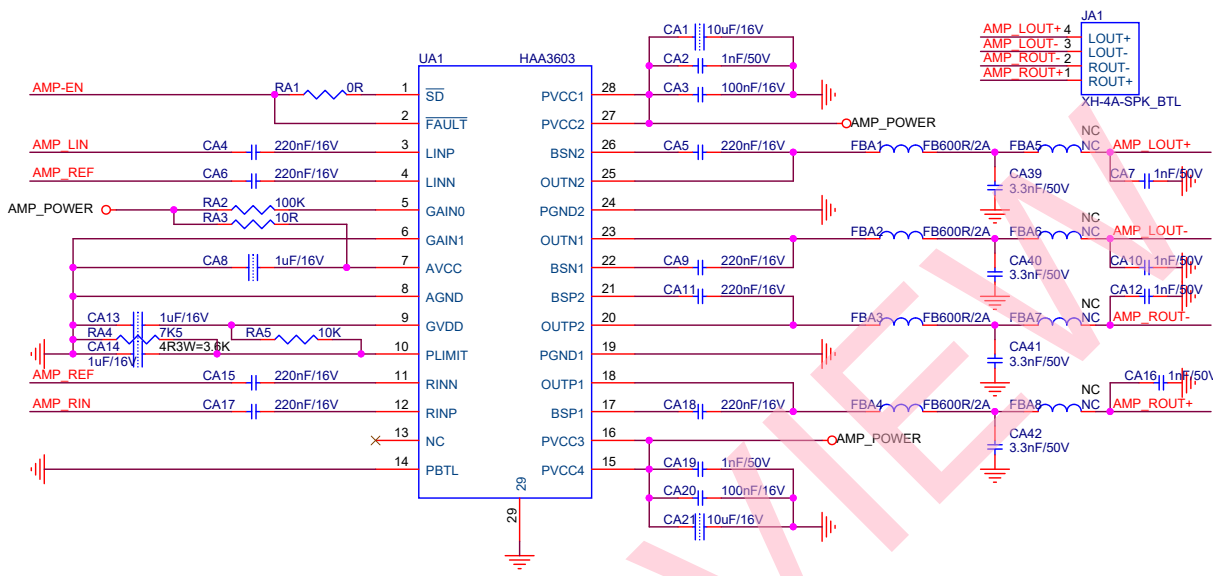
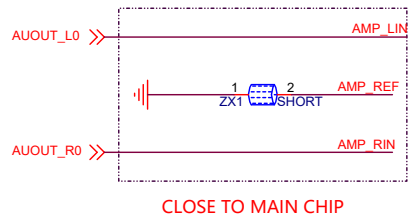


CLK

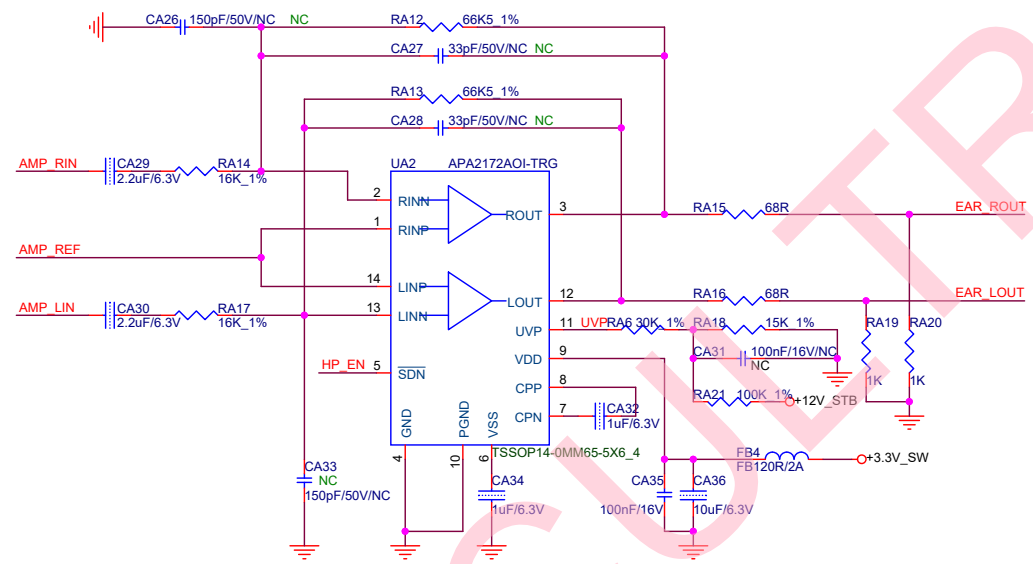




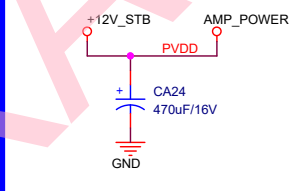
AMPLIFY



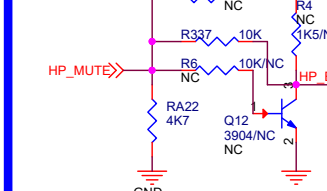
Headphone OP



POWER



HP_MUTE



MUTE



EARPHONE

